

## SOLIOS EV-CL CAMERA LINK FRAME GRABBER

MIL-LITEXLNX

- Frame grabber with available support for 10-taps
- Captures from frame and line scan cameras
- Programmable FPGA (optional)
- PCI Express x4 interface



### PRODUCT DESCRIPTION

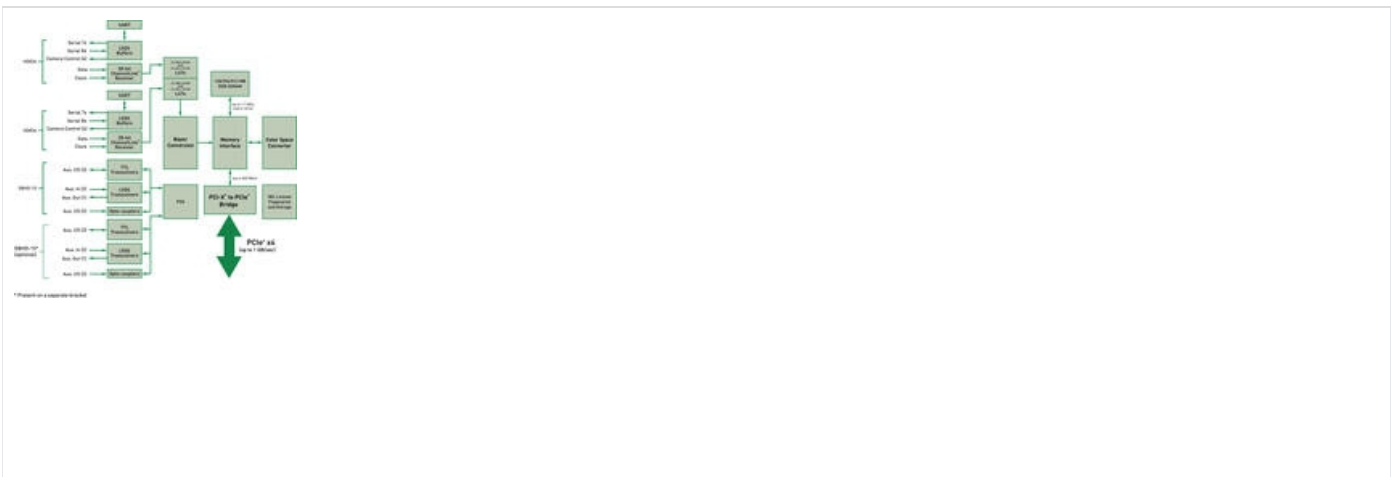
Matrox Solios eV-CL ushers in a new level of value for Camera Link® frame grabbers. It provides connectivity to the most high-performance, multi-megapixel area and line scan Camera Link® cameras on the market augmented with onboard Bayer interpolation (eV-CLB/CLBL), color space conversions and look-up tables. The Matrox Solios eV-CL provides all these capabilities at the most attractive price point yet.

### Technical specifications

Connectivity (ev-CLB/CLF)	Connectivity (ev-CLBL/CLFL)
<ul style="list-style-type: none"> <li>• two(2) mini Camera Link® (HDR) connectors</li> <li>• one (1) DBHD-15 male connector                             <ul style="list-style-type: none"> <li>• three (3) TTL configurable auxiliary I/Os</li> <li>• two (2) LVDS auxiliary inputs</li> <li>• one (1) LVDS auxiliary output</li> <li>• two (2) opto-isolated auxiliary inputs</li> </ul> </li> <li>• optional add on DBHD-15 male connector                             <ul style="list-style-type: none"> <li>• three (3) TTL configurable auxiliary I/Os</li> <li>• two (2) LVDS auxiliary inputs</li> <li>• one (1) LVDS auxiliary output</li> <li>• two (2) opto-isolated auxiliary inputs</li> </ul> </li> <li>• optional add on DB-91 female connector                             <ul style="list-style-type: none"> <li>• one (1) TTL configurable auxiliary I/O</li> <li>• one (1) LVDS auxiliary input</li> <li>• two (2) opto-isolated auxiliary inputs</li> </ul> </li> </ul>	<ul style="list-style-type: none"> <li>• two(2) Camera Link® (MDR) connectors</li> <li>• one DB-44 and DB-9 connector                             <ul style="list-style-type: none"> <li>• six (6) TTL configurable auxiliary I/Os</li> <li>• four (4) LVDS configurable auxiliary inputs</li> <li>• four (4) LVDS configurable auxiliary outputs</li> <li>• separate LVDS pixel clock, hsync and vsync outputs</li> <li>• four (4) opto-isolated configurable auxiliary inputs</li> </ul> </li> </ul>

### Block diagram

Matrox Solios eV-CLB



### Matrox Solios eV-CLBL



### Matrox Solios eV-CLF



### Matrox Solios eV-CLFL

